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- In a multi-reference switching amplifier of the type wherein switching
 devices control power supplied to a load in response to an input data stream having a sign, a simplified output topology, comprising:
- a plurality of effective references; and circuitry for summing the references at the load through linear and switched control of the switching devices.
- 2. The simplified output topology of claim 1, wherein the effective references are independently-modulated.
 - 3. The simplified output topology of claim 1, further including:
- a data separator operative to separate the input data stream into course- and fineresolution data;
- 4 circuitry for modulating a first one of the effective references on one side of the load as a function of the coarse-resolution data; and
- 6 circuitry for modulating a second one of the effective references on the other side of the load as a function of the fine-resolution data under control of the sign of the data.
- 4. The simplified output topology of claim 3, wherein the first one of the effective references is supply rail.
- 5. The simplified output topology of claim 3, wherein the second one of the effective references is a voltage divider.